

FIG. 1A  
(PRIOR ART)

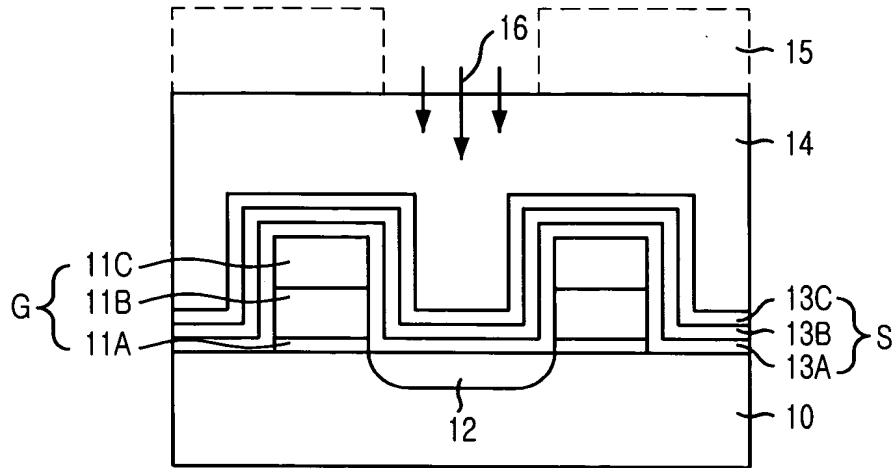
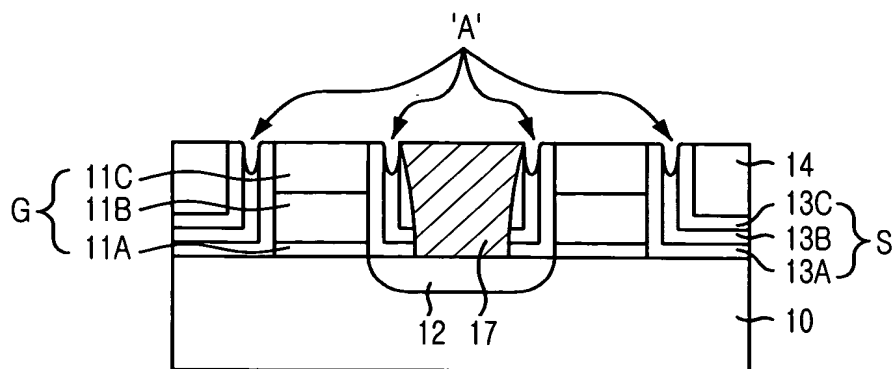


FIG. 1B  
(PRIOR ART)



This cross-sectional view shows a central gate structure 21 with a hatched core 12 and a gate oxide 17. The gate is flanked by two regions, 'A' and 'B', which are part of a larger structure 20. The structure 20 includes layers 19, 18, 14, 13C, 13B, and 13A, which are collectively labeled as 'S'. The entire device is built on a substrate 10. The gate structure 21 is positioned between two regions of the substrate 10, which are labeled 12 and 17. The regions 12 and 17 are separated by a central gap. The gate structure 21 is positioned between two regions of the substrate 10, which are labeled 12 and 17. The gate structure 21 is positioned between two regions of the substrate 10, which are labeled 12 and 17.

[illegible]

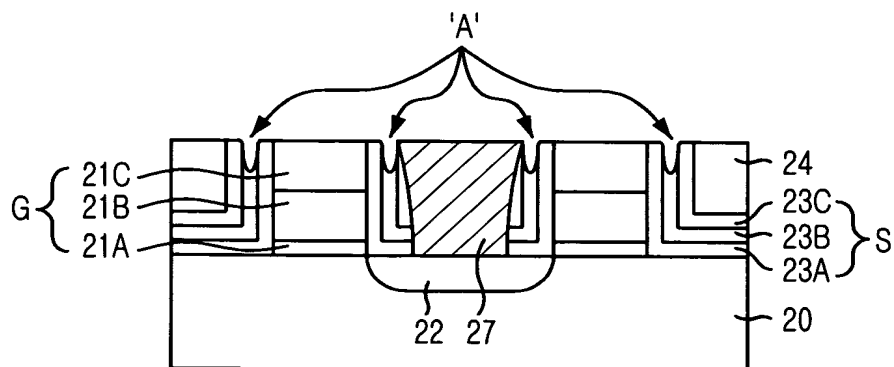




FIG.3A

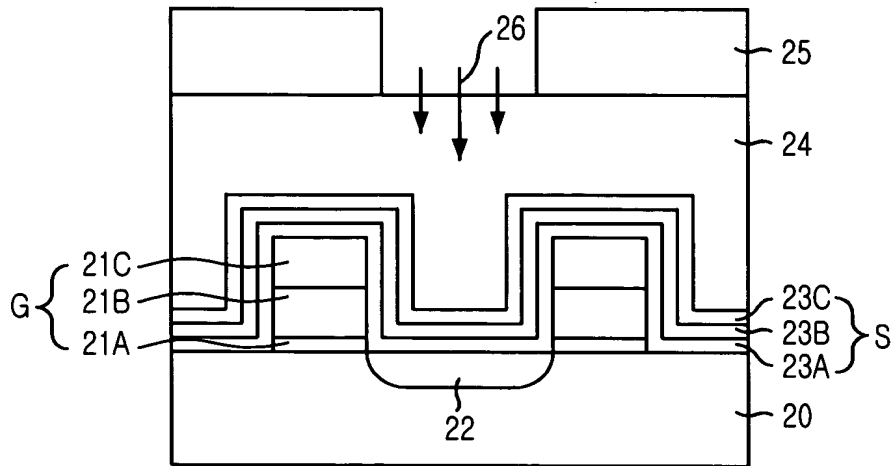


FIG.3B

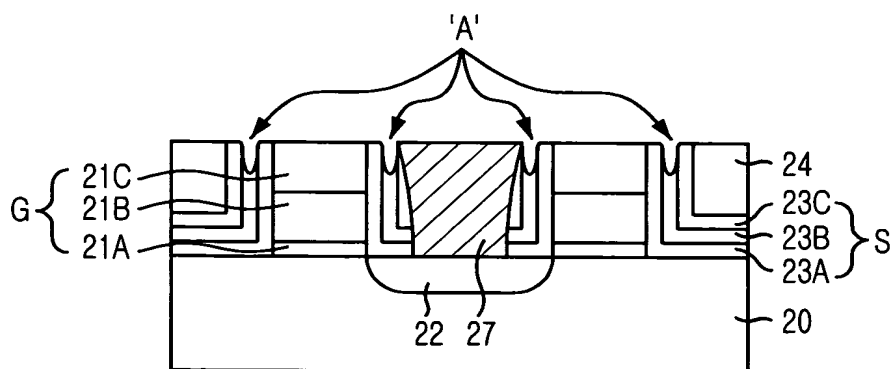


FIG.3C

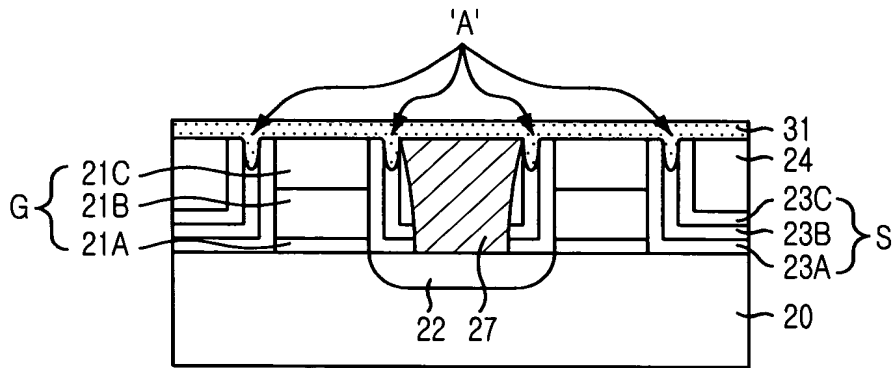


FIG.3D

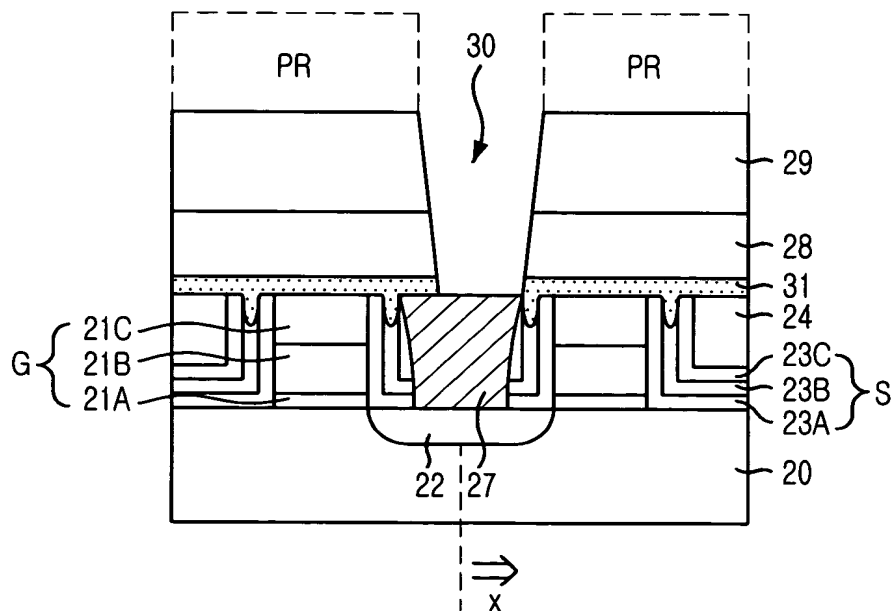


FIG.3E

